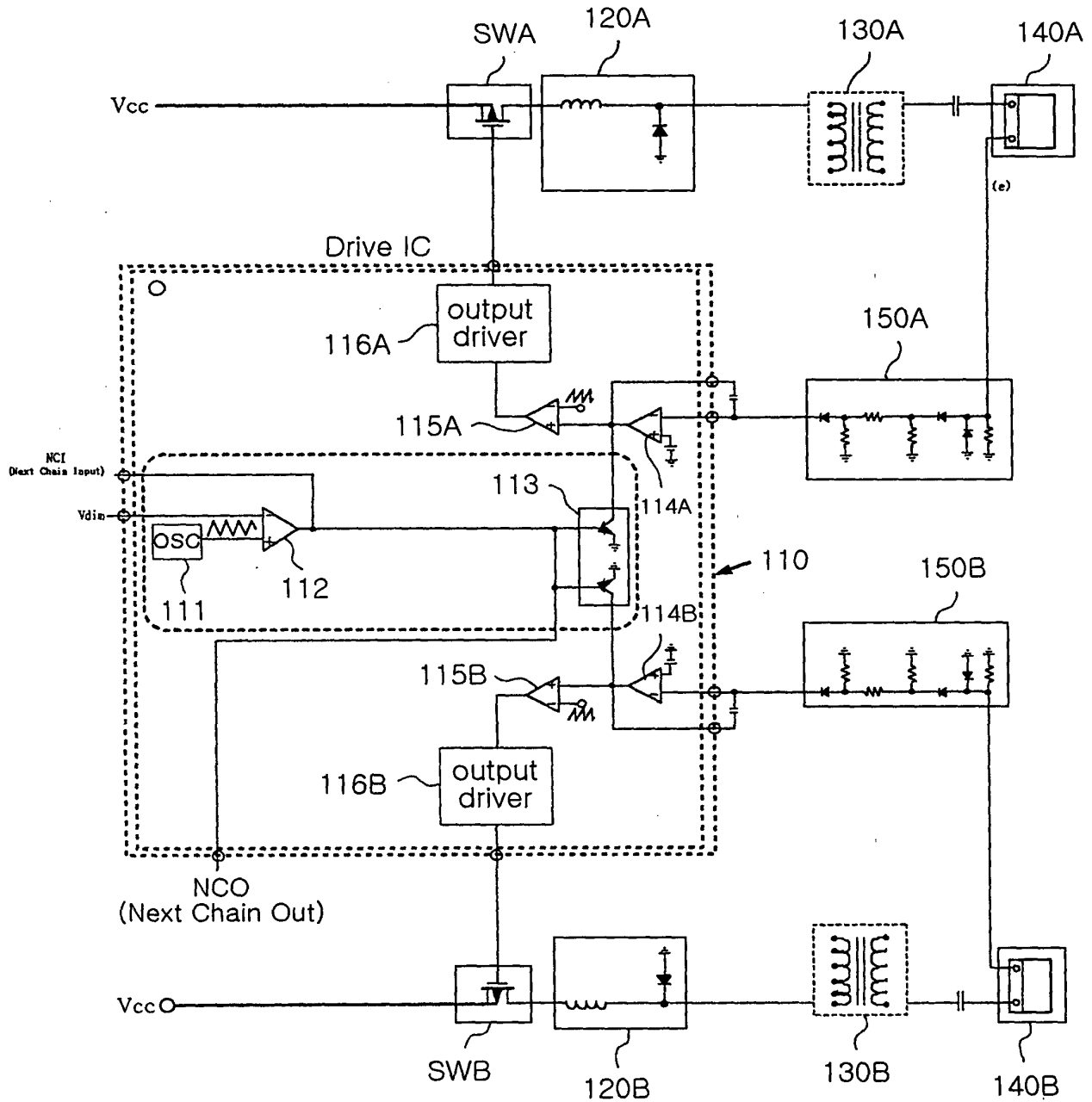


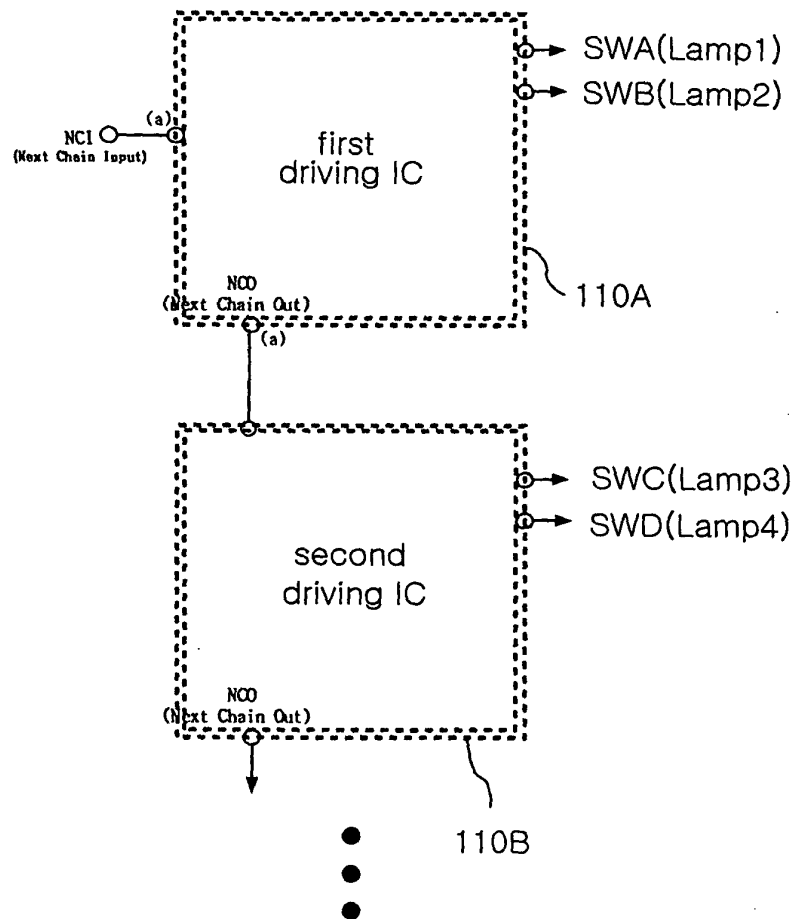
1/11



PRIOR ART

FIG. 1

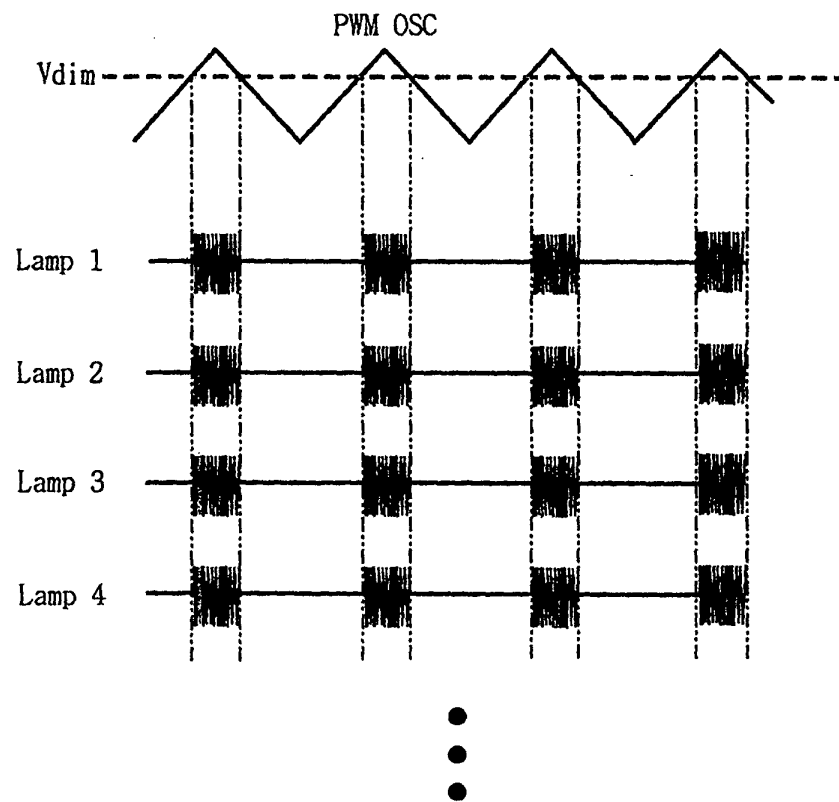
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PRIOR ART

FIG. 2

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PRIOR ART

FIG. 3

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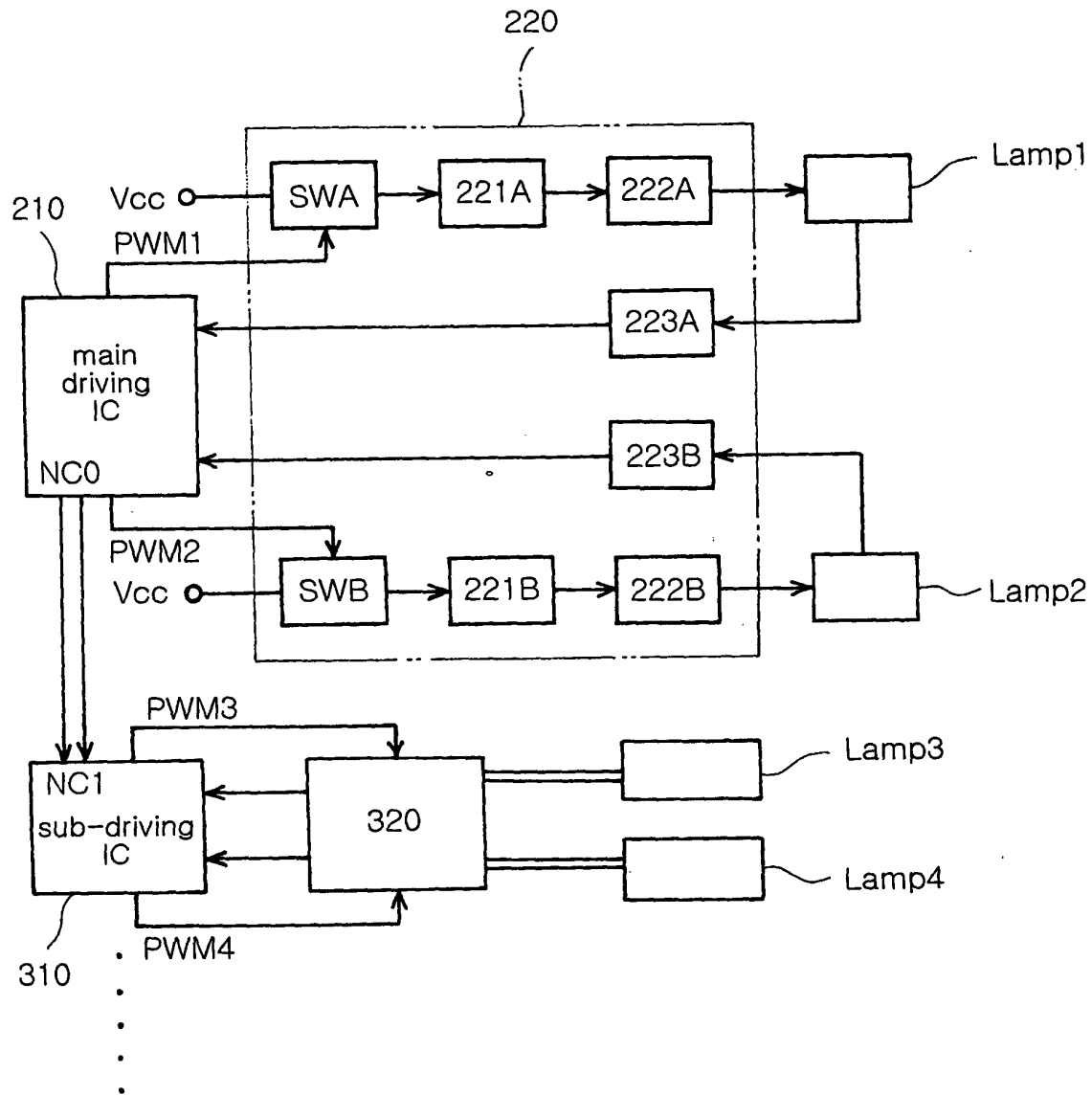


FIG. 4

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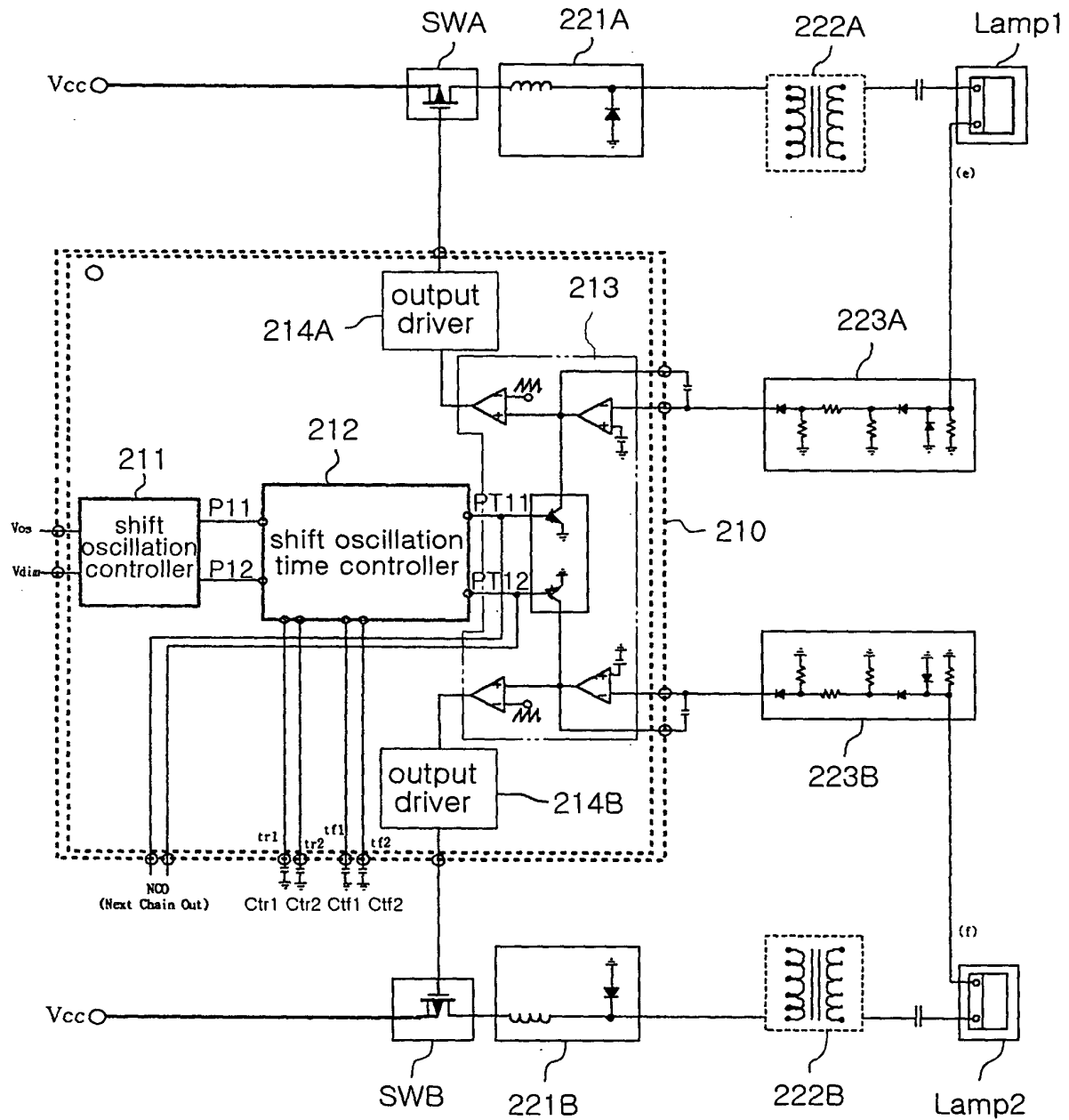


FIG. 5

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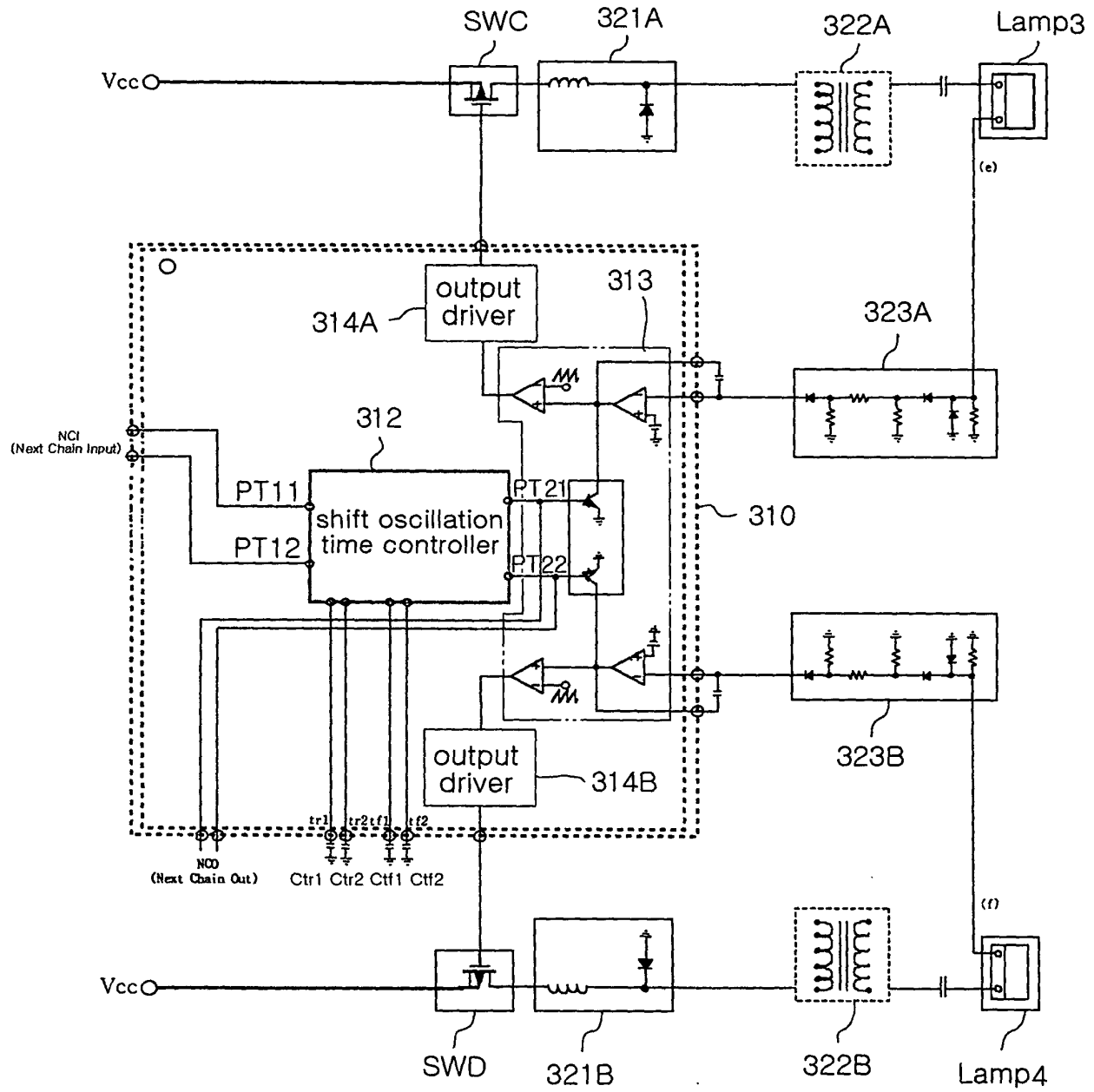


FIG. 6

The diagram shows a circuit for a first current source 211. It contains a PWM OSC block, two operational amplifiers 211A and 211B, and two resistors 211C and 211D. The circuit is powered by Vos and Vdim. The outputs are P11 and P12.

FIG. 7

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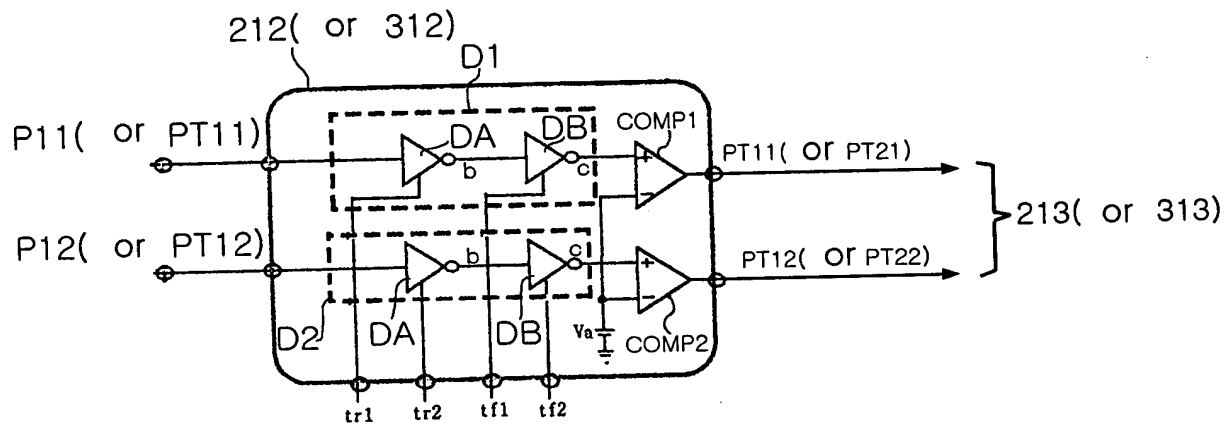


FIG. 8



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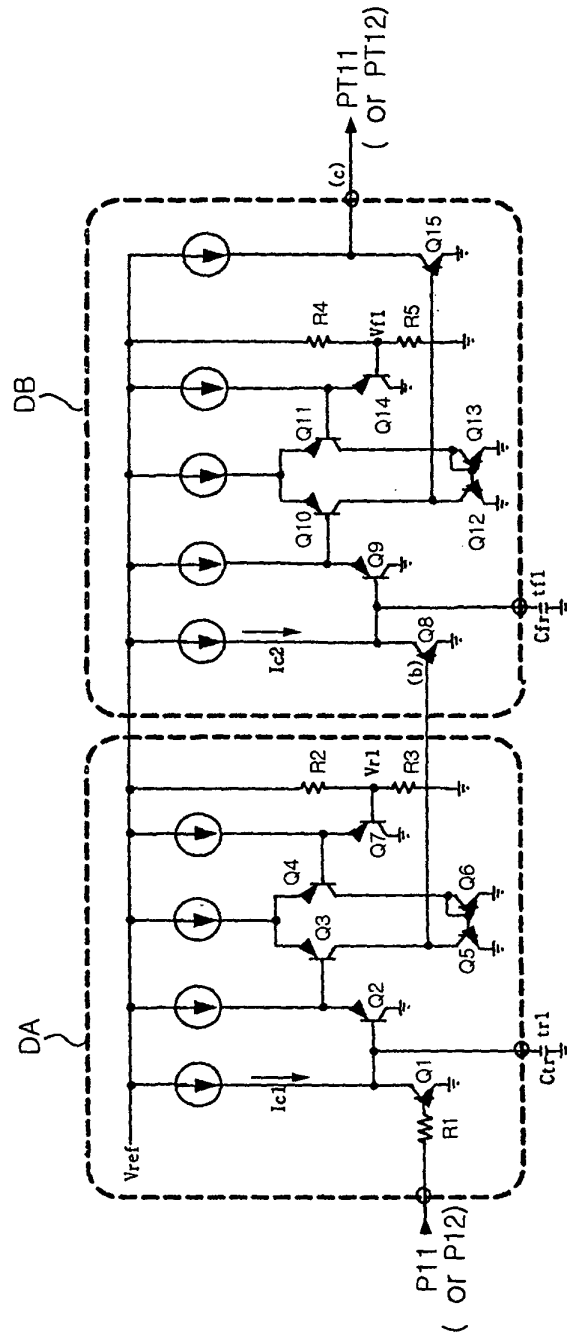


FIG. 9

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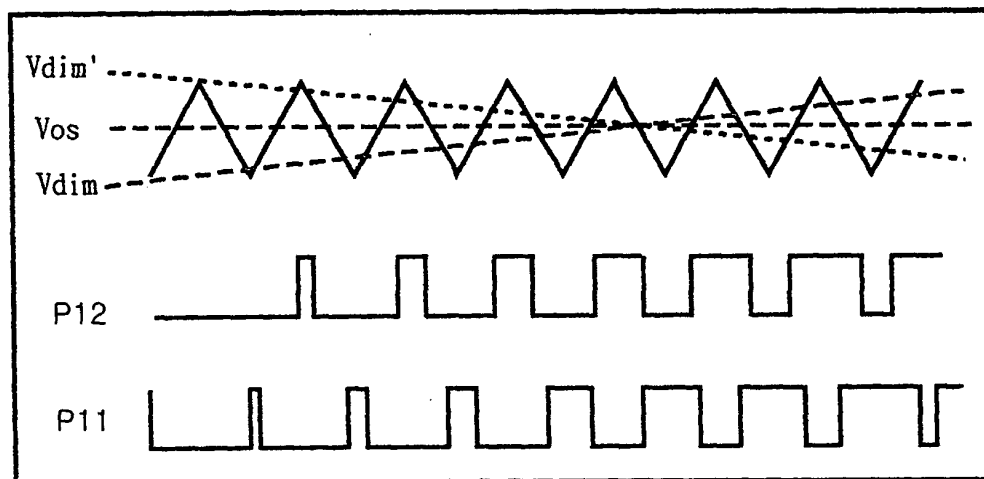


FIG. 10

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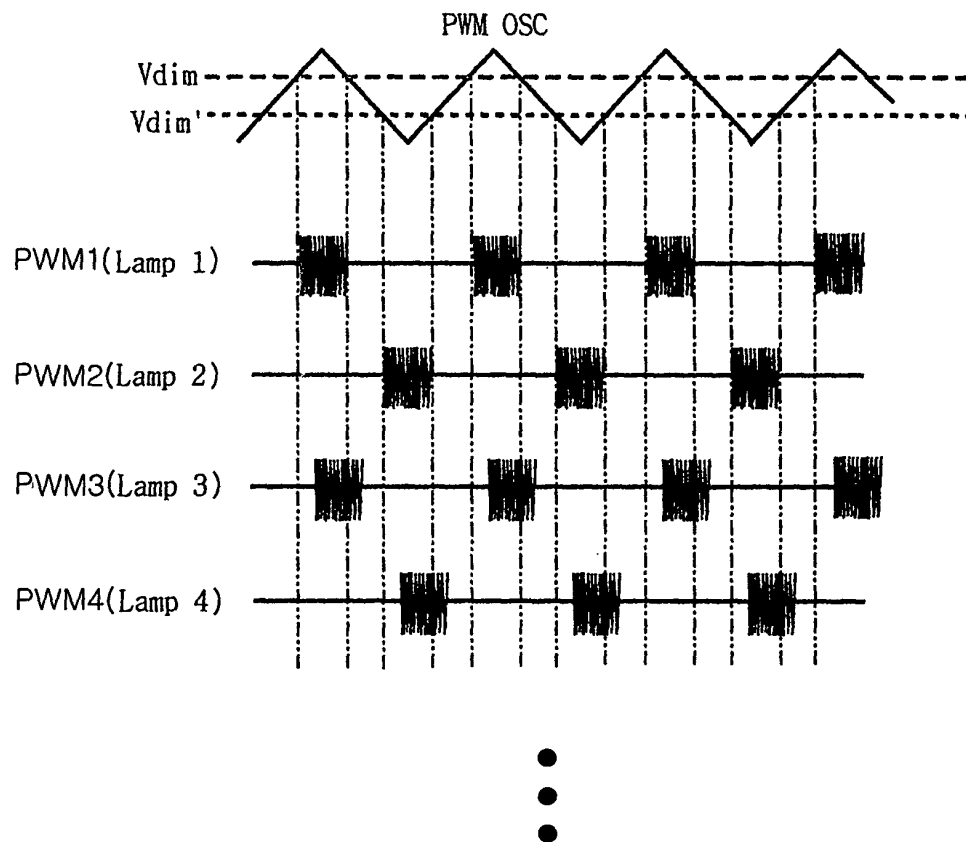


FIG. 11